

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Masaki KOMAKI

Serial No.: 09/939,752

Filed: August 28, 2001

For: FUNDAMENTAL CELL, SEMICONDUCTOR INTEGRATED  
DEVICE, WIRING METHOD AND WIRING APPARATUS

Confirmation No: 3840

Art Unit: 2815

Examiner: J. Nguyen

Docket No.: 024016-00014

PRELIMINARY AMENDMENT WITH FILING OF RCE

Commissioner for Patents  
Washington, D.C. 20231

March 25, 2003

Dear Sir:

Prior to continued examination of the application, please amend the above-  
identified application as follows:

IN THE CLAIMS:

Please amend and add the following claims:

B1  
1. (Twice Amended) A fundamental cell, used as a basic unit in layout  
of a semiconductor integrated circuit device, comprising:  
no fixed wiring for commonly wiring between fundamental cells in a  
hierarchy where fundamental cells are formed and  
connector terminals to be connected to upper wiring layers.

B2  
8. (Twice Amended) A semiconductor integrated circuit device,  
comprising:  
a fundamental cell, used as a basic unit in layout, having no fixed wiring to  
be commonly wired between the basic units in a hierarchy where fundamental

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